

S/N 10/612,793



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Arup Bhattacharyya
Serial No.: 10/612,793
Filed: July 02, 2003
Title: HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL

Examiner: Fazli Erdem
Group Art Unit: 2826
Docket: 1303.111US1

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

ARUP BHATTACHARYYA

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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Date 11-29-04

By Marvin L. Beekman
Marvin L. Beekman
Reg. No. 38,377

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 29 day of November, 2004.

Name KACIA LEE

Signature Kacia Lee

<p>Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)</p> <p style="text-align: center; font-size: small; margin-top: 10px;">O P E DEC 01 2004 PATENT & TRADEMARK OFFICE</p>	<p><i>Complete if Known</i></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td style="width: 50%;">10/612,793</td> </tr> <tr> <td>Filing Date</td> <td>July 2, 2003</td> </tr> <tr> <td>First Named Inventor</td> <td>Bhattacharyya, Arup</td> </tr> <tr> <td>Group Art Unit</td> <td>2826</td> </tr> <tr> <td>Examiner Name</td> <td>Erdem, Fazli</td> </tr> </table>	Application Number	10/612,793	Filing Date	July 2, 2003	First Named Inventor	Bhattacharyya, Arup	Group Art Unit	2826	Examiner Name	Erdem, Fazli
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Examiner Name	Erdem, Fazli										
<p>Sheet 1 of 1 Attorney Docket No: 1303.111US1</p>											

US PATENT DOCUMENTS				
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-2002/0048190-A1	04/25/2002	King, T.	12/21/2001
	US-2002/0105023-A1	08/08/2002	Kuo, T. C., et al.	05/18/2001
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	US-2004/0041206-A1	03/04/2004	Bhattacharyya, A.	04/29/2003
	US-5,488,243	01/30/1996	Tsuruta, K. , et al.	12/03/1993
	US-5,557,569	09/17/1996	Smayling, M. C., et al.	05/25/1995
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	US-6,600,188	07/29/2003	Jiang, C. , et al.	02/26/2002
	US-6,638,627	10/28/2003	Potter, M. D.	12/10/2001

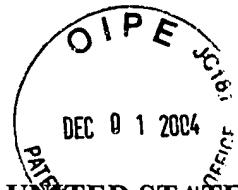
FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²

EXAMINER

DATE CONSIDERED

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COMMUNICATION CONCERNING RELATED APPLICATION(S)

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/232855	August 30, 2002	1303.072US1	GATED LATERAL THYRISTOR-BASED RANDOM ACCESS MEMORY CELL (GLTRAM)
10/232848	August 30, 2002	1303.073US1	ONE-DEVICE NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/232846	August 30, 2002	1303.080US1	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/425483	April 29, 2003	1303.105US1	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/925120	August 24, 2004	1303.118US1	MEMORY CELL WITH TRENCHED GATED THYRISTOR
10/931367	August 31, 2004	1303.080US2	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL
10/930440	August 31, 2004	1303.105US2	ONE TRANSISTOR SOI NON-VOLATILE RANDOM ACCESS MEMORY CELL

Respectfully submitted,

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KACIA LEE
Name

Kacia Lee
Signature